Electronic Acknowledgement Receipt EFS ID: 2064927 Application Number: 10709754 International Application Number: Confirmation Number: 3753 A SYSTEM AND METHOD OF PROVIDING ERROR DETECTION AND Title of Invention: CORRECTION CAPABILITY IN AN INTEGRATED CIRCUIT USING REDUNDANT LOGIC CELLS OF AN EMBEDDED FPGA First Named Inventor/Applicant Name: JOHN M. COHN Customer Number: 23389 Filer: Steven Fischman/Grace Colucci Filer Authorized By: Steven Fischman Attorney Docket Number: BUR920040001US1 Receipt Date: 09-AUG-2007 Filing Date: 26-MAY-2004 Time Stamp: 11:50:01 Application Type: Utility under 35 USC 111(a) Payment information: Submitted with Payment no

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